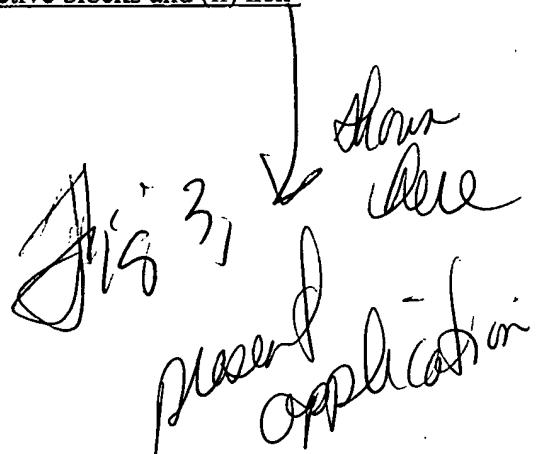


IN THE CLAIMS

1. (Currently Amended) A memory apparatus comprising:
a first storage region from which data can be read and into which data can be
written, in accordance with instructions made by a user, [[and]]
a second storage region from which data can be read and into which data can be
written by a data-processing apparatus, said second storage region having a user-use prohibition
table which is normally inaccessible to said user and which has a plurality of addresses of data
items in which one address designates one or more defective locations in said memory apparatus,
wherein said user or said data-processing apparatus is prohibited from accessing said one or
more defective locations contained in said user-use prohibition table; and
a conversion table which includes logic addresses which are assigned only to
blocks in said first storage region and are not assigned to (i) defective blocks and (ii) non-
defective blocks in said second storage region.

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show all
present application